



# Muzaffarpur Institute of Technology (MIT), Muzaffarpur

(Under the Department of Science & Technology Govt. of Bihar, Patna)

## Department of Electrical Engineering

### DEPARTMENT TIME TABLE

Effective from 18-12-2023

Room no.15

### ELECTRICAL 5<sup>TH</sup> SEM.

DAY/TIME	10:00-11:00	11:00-12:00	12:00-13:00	13:00-14:00	14:00-16:00	15:00-16:00	16:00-17:00
MONDAY	CS (DY)	CS (DY)	PS I (YNS)	L U N C H	POWER SYSTEM I LAB (YNS+CS) (G2)		
TUESDAY	MP(RPG)	LCS (MS)	LCS (MS)		POWER ELECTRONICS LAB(AKS+AK) (G2)/ MICROPROCESSOR LAB (RPG+SU) (G1)		
WEDNESDAY	MP(RPG)	PE (AKS)	PS I (YNS)		MICROPROCESSOR LAB (RPG+SU) (G2)		
THURSDAY	PS I (YNS)	PE (AKS)			CONTROL SYSTEM LAB (DY+SA) (G2)/ POWER SYSTEM I LAB (YNS+CS) (G1)		
FRIDAY	LCS (MS)	CS (DY)			CONTROL SYSTEM LAB (AKS+AK) (G1)		
SATURDAY	PE (AKS)	MP(RPG)			POWER ELECTRONICS LAB(AKS+AK) (G1)		

G1:- ROLL NO. 21EE01-21EE36

G2:- ROLL NO. 21EE37-REST

SUBJECT NAME		FACULTY NAME	
LCS	LINEAR CONTROL SYSTEM	MS	MD. SHAHAZAM
CS	CONTROL SYSTEM	DY	DEEPSHIKHA YADAV
MP	MICROPROCESSOR	RPG	DR. RAMJEE PRASAD GUPTA
PE	POWER ELECTRONICS	AKS	MR. ANKIT KUMAR SINGH
PS-I	POWER SYSTEM I	YNS	DR. Y N SHARMA
	POWER SYSTEM I LAB	YNS/CS	DR. Y N SHARMA/CHETNA SAGAR
	CONTROL SYSTEM LAB	DY/MS	DEEPSHIKHA YADAV/MD.SHAHAZAM
	MICROPROCESSOR LAB	RPG/SU	DR. RAMJEE PRASAD GUPTA/SHIVANGI UTKARSH
	POWER ELECTRONICS LAB	AKS/AK	ANKIT KR SINGH/DR.ABHA KUMARI

Time Table In-charge (Dept. of EE)  
16/12/23

HOD(EED)  
16/12/23

