Muzaffarpur Institute of Technology (MIT), Muzaffarpur

(Under the Department of Science & Technology Govt. of Bihar, Patna)

Department of Information Technology

TIME TABLE FOR B. TECH 5th SEMESTER - BATCH 2023

Venue: IT Building, Classroom No: - 1   **Effective From – January 15, 2026**

TIME ↓ DAY →	10:00-11:00	11:00-12:00	12:00-1:00	1:00-2:00	2:00-3:00	3:00-4:00	4:00-5:00
MONDAY		Design and Analysis of Algorithm (G1) – Lab I		L U N C H	Microprocessor (G1)		A_1 – (MP)
		Compiler Design (G2) – Lab II					
TUESDAY		Design and Analysis of Algorithm (G1) – Lab I			A_2 – (MP)	B_1 – (CD)	D_1 – (OS)
		Operating System (G2) – Lab II					
WEDNESDAY		Operating System (G1) – Lab I			A_3 – (MP)	C_1 – (DAA)	B_2 – (CD)
		Compiler Design (G2) – Lab II					
THURSDAY		Operating System (G1) – Lab I			F_1 – (PSD)	D_2 – (OS)	C_2 – (DAA)
		Design and Analysis of Algorithm (G2) – Lab II					
FRIDAY	B_3 – (CD)	Operating System (G2) – Lab II			F_2 – (PSD)	D_3 – (OS)	C_3 – (DAA)
		Compiler Design (G1) – Lab I					
SATURDAY	E_1 – (S)	Design and Analysis of Algorithm (G2) – Lab II			Microprocessor (G2)		F_3 – (PSD)
		Compiler Design (G1) – Lab I					

S. No.	Course Code / Subject Name	Faculty Name
A	100504 / Microprocessor	Faculty, EE
B	106501 / Compiler Design	Mrs. Naincy Priya
C	106502 / Design and Analysis of Algorithm	Mr. Umesh Kumar
D	106503 / Operating System	Mr. Ravi Ranjan
E	106504 / Seminar	Dr. Shweta Kumari
F	106508 / Professional Skill Development	Faculty, AS&H
G	106510P / Summer Entrepreneurship - II	Mr. Ravi Ranjan

Laboratory / Faculty Name
Microprocessor / Faculty EE
Compiler Design / Mrs. Naincy Priya
DAA / Mr. Umesh Kumar
Operating System / Mr. Ravi Ranjan
Lab Assist.: - Mr. Rajesh Kumar, Mr. Gulshan Kumar, Mr. Rikesh Kumar G1 → Reg. 1 - 32 and G2 → Reg. 33 and above including LE Students

Ashish Kumar
Time Table In-charge

By Order
Head of Department (IT)